



US 20170102730A1

(19) **United States**(12) **Patent Application Publication** (10) **Pub. No.: US 2017/0102730 A1**  
(43) **Pub. Date: Apr. 13, 2017**(54) **SYSTEM-ON-CHIP TO SUPPORT FULL  
HANDSHAKE AND MOBILE DEVICE  
HAVING THE SAME**(30) **Foreign Application Priority Data**

Dec. 8, 2014 (KR) ..... 10-2014-0175083

(71) Applicant: **Samsung Electronics Co., Ltd.,  
Suwon-si (KR)****Publication Classification**(72) Inventors: **Youn-Sik CHOI**, Yongin-si (KR);  
**Jin-Ook SONG**, Seoul (KR); **Ho-Yeon  
JEON**, Hwaseong-si (KR); **Jae-Gon  
LEE**, Yongin-si (KR)(51) **Int. Cl.**  
**G06F 1/06** (2006.01)  
**H04L 7/00** (2006.01)  
**H04J 3/06** (2006.01)  
(52) **U.S. Cl.**  
CPC ..... **G06F 1/06** (2013.01); **H04J 3/0685**  
(2013.01); **H04L 7/0008** (2013.01)(73) Assignee: **Samsung Electronics Co., Ltd.,  
Suwon-si (KR)**(21) Appl. No.: **15/388,366**(57) **ABSTRACT**(22) Filed: **Dec. 22, 2016****Related U.S. Application Data**(63) Continuation of application No. 14/824,685, filed on  
Aug. 12, 2015, now Pat. No. 9,582,026.(60) Provisional application No. 62/069,543, filed on Oct.  
28, 2014, provisional application No. 62/057,388,  
filed on Sep. 30, 2014.

A system-on-chip (SoC) comprises a clock management unit (CMU) including a first clock generator and a second clock generator, the first and second clock generators being configured to generate clock signals. The SoC comprises at least one logic block configured to request the clock signals from the CMU according to a full handshake method and receive the clock signals from the CMU in response to the request. The first clock generator and the second clock generator are configured to communicate according to the full handshake method.

